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Wavecom Wireless Seminar Series in Australia and New Zealand

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Braemac has an in house cable assembly facility

Flexible, Powerful Solutions for Machine-to-Machine Authentication

Welcome to our March 2009 edition of Braemac Product News

WAVECOM along with Braemac invite you to join our 2009, Half day Wireless Seminar.

We still have a few seats left for our Wavecom Wireless Seminar Series to be held next week. Click to confirm your seat in Syd, Mel and Auckland. This event is free and has Wavecom factory engineering experts presenting latest product releases from Wavecom at the seminar.



WAVECOM along with Braemac invite you to join our 2009, Half day Wireless Seminar.

Learn all about the latest in Wireless CPU's.

Wavecom is a leading provider of embedded wireless technology for M2M (machine-to-machine) communication. Wavecom provide's a range of GSM/GPRS, CDMA, EDGE and 3G Wireless CPUs; programmable processors which also act as wireless modules or wireless modems.

These are backed by a C and Lua-based cellular wireless software suite which includes a real-time operating system (RTOS), a software development environment based on Eclipse™, and several Plug-Ins (GPS, TCP/IP, security, Bluetooth™, Lua script and more).

Wavecoms solutions are used for automotive, telematics, smart metering, fleet management, GSM/GPS/satellite tracking, wireless alarms, wireless POS (point of sales), WLL (fixed voice), remote monitoring and many other M2M applications.

Registration: 8.30am
Seminar: 9.00am - 12.00pm
Seats are limited. Cost = Free

Agenda

Wavecom Portfolio & Roadmap focusing on newly announced products.

Venues / Dates

Sydney - 16 March 2009
 Mercure Sydney Parramatta
 106 Hassall Street
 Rosehill NSW 2142

M2M Studio Demonstration
Wavecom/Braemac Support Structure (OTS) and added professional services (schematic, layout & SW reviews and certification)

IDS and Demonstrations

[Confirm](#) your registration for Sydney

[Confirm](#) your registration for Melbourne

[Confirm](#) your registration for Auckland

Please Confirm your registration!

Melbourne - 17th March 2009
Clarion Hotel on Canterbury
326 Canterbury Road
Forest Hill, 3131

Auckland - 19th March 2009
Waipuna Hotel Conference Center
58 Waipuna road
Auckland

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Smart wireless. Smart business.

Altera's Stratix IV FPGAs Named Product of the Year by Electronic Products China

Stratix IV Family Continues to Receive Worldwide Acclaim

Altera Corporation today announced its 40-nm Stratix® IV FPGAs received the Product of the Year award from Electronic Products China magazine. This award marks the sixth award the Stratix IV family has received since its announcement in May 2008.



Presented annually, Electronic Products China's Product of the Year Award is given to products that show significant progress in technology, innovative design or offer distinct price/performance improvements compared to competing devices. Stratix IV FPGAs deliver market leadership in density, performance and power, and offer an ideal solution for high-end digital applications in several markets, including wireless and wireline communications, military and broadcast.

"The Stratix IV FPGAs provide extraordinary densities, performance and low-power leadership," said Karen Cui, executive editor-in-chief, Electronic Products China. "Also, by shipping on schedule, these devices will help customers meet the diverse high-end application needs in a large number of markets. The Stratix IV FPGAs are very deserving of this award."

Stratix IV FPGAs include three variants, an enhanced (E) version and transceiver (GX) and (GT) versions. The family offers up to 680K logic elements, 22.4 Mbits of internal RAM and 1,360 18x18 embedded multipliers. The devices also utilize the latest architecture innovations and process techniques to deliver the lowest power and the highest performance of any high-end FPGA.

Stratix IV GX FPGAs feature up to 48 transceivers operating up to 8.5 Gbps, enabling the development of next-generation, high-bandwidth communication infrastructure. Optimized specifically for 40G and 100G applications, Stratix IV GT FPGAs have up to 48 multi-Gigabit transceivers with 24 operating at 11.3 Gbps. Stratix IV GX FPGAs incorporate up to four hard intellectual property (IP) cores for PCI Express Gen1 and Gen2 (x1, x4 and x8), and Stratix IV GT FPGAs incorporate one hard IP core for PCI Express Gen1 and Gen2 (x1 and x4). Both variants also support a wide range of protocols including Serial RapidIO®, 40G/100G Ethernet, XAUI, CPRI (including 6G CPRI), CEI-6G, GPON, SFI-5.1 and Interlaken. For more information on the market-leading features in Altera® Stratix IV FPGAs, visit www.altera.com/pr/stratix4.

[For further information click here.](#)

Hitachi Display Products Group (DPG) has successfully unveiled a series of new

TFT display modules.



The following displays are scheduled to be released to the market during 2009: a 6.5" VGA TFT, a 7.0" WVGA TFT and a 8.0" WVGA TFT both featuring Hitachi's IPS Pro technology. Also announced were two high resolution displays targeting medical and professional monitoring applications: the new 19" WSXGA+ and a 21.2" QXGA TFT display modules also both feature Hitachi's IPS-Pro technology. These new product announcements re-emphasize Hitachi's continued commitment to providing high quality medium and large size TFT display modules featuring unrivalled optical performance.

Hitachi's continuous improvement of IPS LCD technology now provides the best LCD solution on the market for the requirements of professional, high end and multi-user imaging. The latest evolution, IPS-Pro enables exceptional colour saturation, colour stability, excellent contrast and deeper black levels with a 176 degrees wide vertical and horizontal viewing angle.

[For further information click here.](#)

Silicon Labs Expands MCU Portfolio with High Pin-Count, Touch-Sensing Device

C8051F700 Enables Robust, Cost-Effective Capacitive Touch Sensing

[Silicon Laboratories Inc.](#), a leader in high-performance, analog-intensive, mixed-signal ICs, today announced the introduction of its [C8051F7xx family](#) of high pin-count MCUs for cost-sensitive, high I/O embedded systems. This family is the first to offer a new, patent-pending touch sensing feature that is robust, accurate, responsive and easy to configure. Offering up to 54 general purpose I/O pins and a 25 MIPS 8051 CPU, the C8051F7xx family brings a high level of processing power and flexibility to applications such as industrial controls, security systems, residential HVAC, home appliances, keyboards, cash machines and fax/printer/scanner front panels.

The industry's fastest touch sense on-chip peripheral uses a capacitance-to-digital converter (CDC) with a 40 us acquisition time that when combined with the 25 MIPS CPU on the C8051F7xx enables sophisticated human interface functions, even when large arrays of touch sense elements are used. To combat issues in electrically noisy environments, the CDC offers best-in-class noise immunity, insuring reliable performance. With up to 32 touch sensing inputs, featuring wake-on-touch, the MCU can be placed in a power saving mode and wake quickly upon touch to save overall system power in applications. An intuitive software GUI allows fast and easy configuration. And, an API library is provided for all common touch sense configurations such as virtual buttons, wheels and sliders.

The C8051F7xx family also provides a number of analog features on board, further reducing system cost. A highly accurate 10-bit successive approximation (SAR) analog-to-digital converter (ADC) with on-chip voltage reference and temperature sensor provides best-in-class analog capability for measurement and control. Up to 54 general purpose I/Os are offered to be used as ADC voltage measurement inputs, capacitive touch-sense inputs and/or digital communications I/Os. A calibrated two percent oscillator with guaranteed accuracy over the full MCU voltage supply and temperature range eliminates the need for an external crystal. In addition, the C8051F7xx family offers byte-erasable EEPROM with 100,000 write/erase cycle endurance guaranteed for storage of frequently updated data.

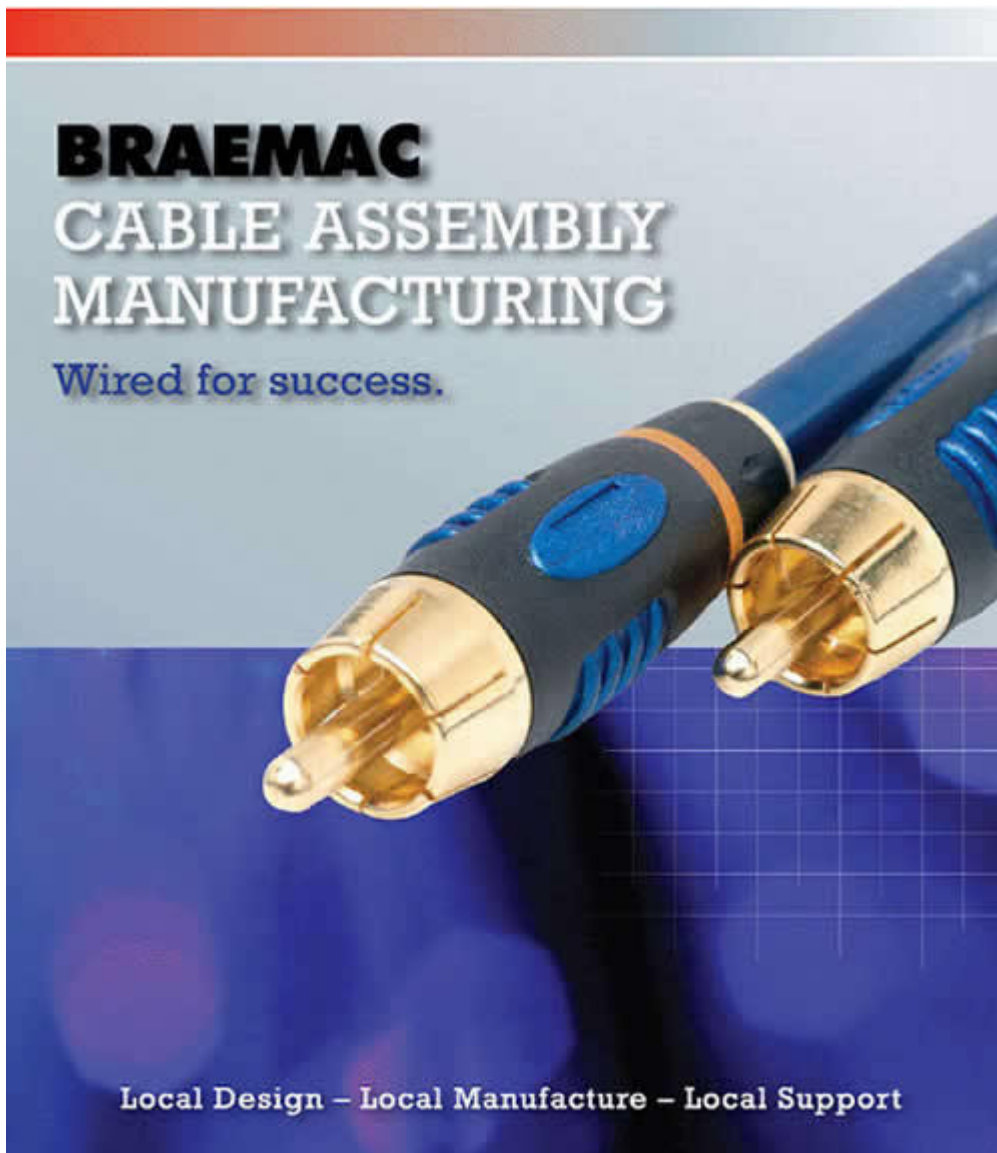
"Silicon Labs' mixed-signal capability has enabled a new level of performance in capacitive touch-sensing," said Mark Thompson, vice president and general manager of Silicon Labs' MCU products. "The C8051F7xx family enables our customers to cost-effectively add best-in-class touch sensing capability to any product requiring higher I/O count and high performance analog peripherals."

Silicon Labs offers a full suite of industry-leading tools to help speed design and accelerate market entry with the C8051F7xx. A complete, low-cost professional development kit includes everything required to immediately begin system design including IDE, target board, cables and power supply. A Silicon Labs GUI is provided for easy and intuitive setup of the capacitive touch sense peripheral to get the desired configuration and performance in minutes. A separate capacitive touch sense evaluation kit provides fast and easy evaluation of the Silicon Labs capacitive sensing solution.

To enter to win a free development kit for the C8051F7xx, click www.silabs.com/C8051F7xxgiveaway.

Braemac has an in house cable assembly facility. We can take your cable requirements and build to your specs.

For a Cable Assembly Information brochure please contact your local Braemac sales representative
Or email cable@braemac.com.au



Flexible, Powerful Solutions for Machine-to-Machine Authentication

Board ID Security Solution



Tracking



Anti-Cloning



Usage Control

Flexible, Powerful Solutions for Machine-to-Machine Authentication



Based on Renesas' proven smart card IC technology and our solid foundation of technology, products and support, the Board ID™ solution is a cost effective, easy to implement approach to drastically reduce risk in machine-to-machine ("M2M") device implementation, while also opening new opportunities for revenue generation. Designed specifically for the M2M market, Renesas' Board ID solutions build on the more than 20 years of security market expertise and our significant track record of deploying M2M authentication solutions. The Board ID is based on Renesas' proven [smart card IC technology](#) which includes EAL4+ and FIPS Level 2 certifications. [Learn More](#)



Renesas Board ID Applications

Based on Renesas' proven smart card IC technology and our solid foundation of technology, products and support, the Board ID Solution facilitates new functions and processes for M2M security while also offering significant benefits for business, industry, hospitals, government, consumers and other M2M markets.

[Learn More](#)

Some application examples include:

- [Tracking](#)
- [Anti-cloning](#)
- [Usage Control](#)
- Others
 - Licensing Control
 - Secure Remote Management

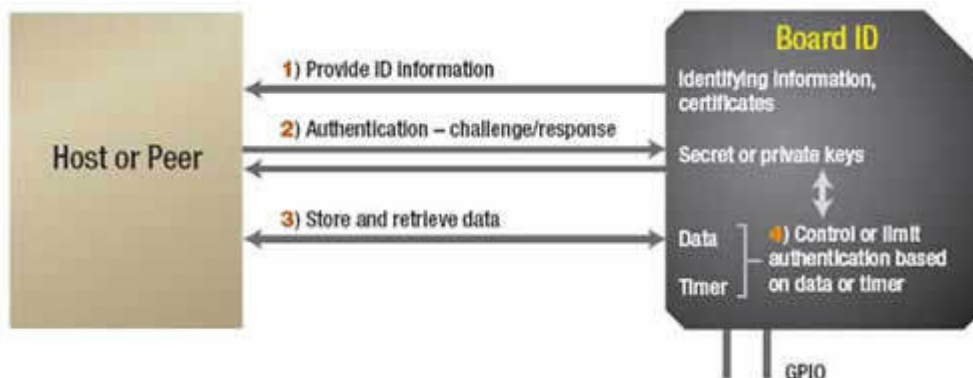
Renesas Board ID Benefits

The Board ID device facilitates new functions and processes for M2M security. It also offers significant benefits for business, industry, hospitals, government, consumers and other M2M markets, relative to traditional machine identification technologies such as EEPROMs.

- Anti-cloning
 - Ensures only a genuine or certified component can be used by a system
- Usage control
 - Ensures host system will only work with a valid peripheral as per the prescribed usage conditions (# times, duration, location)
 - Ensures a host system will only work with equipment that is designated for the desired use; e.g., a phone for sale in Taiwan will only work with a PBX for use in Taiwan
 - Ensures a host system will only work with a subsystem as per the specified usage profile
- Tracking
 - Distribution tracking Ensures warranties and maintenance are provided for valid unit
 - System configuration database; for example, authorize downloading of proper drivers
 - Secure remote downloads
- Numerous other security applications can be implemented

Board ID M2M Authentication Process Solution

In operation, the Board ID based solution makes M2M authentication a fast process. The host authenticates the Board ID chip via a communication link that does NOT have to be secured. When a valid peripheral or subsystem attempts a connection to the host, seven steps must be completed successfully before the host grants connectivity and access to an authorized device. The entire process takes place quickly - typically in a fraction of a second.



Get Your Security Project Started Today

Renesas provides a suite of educational and development tools to help you get your security solution up and running. [Learn more](#)

About Braemac

Braemac is Australia's largest electronic component distributor with offices throughout Australia, New Zealand, USA, Singapore, Hong Kong and the UK. Our product offer includes some of the world's most prestigious suppliers including Atmel, Altera, Hitachi (Renesas), STMicroelectronics, Cirrus Logic, Marvell and Wavecom which allows our customers to choose from a wide selection of quality, well recognised components. Visit Braemac Website



Contact Us

For further information, product data sheets and pricing, please contact your local Braemac sales representative. or email info@braemac.com.au

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